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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

*DC* *None*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

*DC* *None*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 5	TOTAL CLAIMS 37	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>DC</i>				

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## TITLE

Delay-matched ASIC conversion of a programmable logic device

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